JUN 2 1 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10816004	Docket Number	CNTR.2216		
Filed	4/1/2004	/1/2004 Group Art Unit 2116			
Examiner	ANAND PATEL	23669			
Application Title	FREQUENCY-VOLTAGE MECHANISM FOR MICROPROCESSOR POWER MANAGEMENT				
First Named inventor	DARIUS D. GASKINS				

			POVER MANAGEMENT		
First Named Inventor		Inventor	DARIUS D. GASKINS		
			FACSIMILE COVER SHEET		
To Fa	o: ax Number:	Commi 571-273	ssioner for Patents - Central Fax Number 3-8300		
	rom: ax Number:		IAN PATENT GROUP, LLC 60-1986		
P	ages:	58 (incl	luding this cover sheet)		
De	ear Sir:				
CI	FR 1.8 that this c	orrespond	correspondence for the above-identified matter. I hereby certify under 37 dence is being facsimile transmitted to the United States Patent and e of signature shown below.		
	espectfully subm		P, LLC		
/Richard K. Huffman/					
Ву					
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First Named Inventor	DARIUS D. GASKINS				

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

VIA FACSIMILE: 571-273-8300

Mail Stop **AMENDMENT** Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 submitted under 37 CFR 1.97(b) listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seq. Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.

Respectfully submitted,

/ Richard K. Hullman/

By:

RICHARD K. HUFFMAN, P.E.

Registration No. 41,082

Tel: (719) 575-9998

6/2\/2001

Date:



JUN 2 1: 2007

PTO/SB/08A (08-03)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO			Complete if Known		
Substitute for form 1445APTO				Application Number	10816004
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			JRE	Filing Date	4/1/2004
			ANT	First Named Inventor	Darius D. Gaskins
				Group Art Unit	2116
(use as many sheets as necessary			,	Examiner Name	Anand Patel
Sheet	1	of	1 ·	Attorney Docket Number	CNTR.2216

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	ROTEM et al. "Analysis of Thermal Monitor Features of the Intel Pentium M Processor." Downloaded from http://www.cs.virglnia.edu/~skadron/tacs/men.pdf on 3/7/2007.	T
	AB	Intel 64 and IA-32 Architectures Software Developer's Manual. Volume 3A: System Programming Guide, Part 1. November 2006. Pages 13-1, and 13-5 to 13-15.	T
	AC	Intel Pentium M Processor with 2-MB L2 Cache and 533-MHz Front Side Bus. Datasheet. July 2005. Pages 14 and 62-64. Reference Number 305262-002	T
	AD	Wireless Intel SpeedStep Power Manager. "Optimizing power consumption for the Intel PXA27x processor family." Intel. 2004. Pages 1-16.	
	AE	"Enhanced Intel SpeedStep Technology for the Intel Pentium M Processor." Intel. March 2004. Pages 1-12	Τ
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Examiner Signature	Date Considered
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached. This collection of Information Is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450